TITLE

U.S. UTILITY Patent Application O.I.P.E. PATENT DATE

EXAMINER SUBCLASS ART UNIT APPLICATION NO. CONT/PRIOR CLASS 18 BRODA 703 2123 09/677913

SCANNED O

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Method of power distribution analysis for ${\rm I/O}$ circuits in ASIC designs

PTO-2040 12/99

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ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONA	L CLASSIFICATION								
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TERMINAL	DRAWINGS			CLAIMS ALLOWED				
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.			
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subsequent to(date) has been disclaimed.	(Assistant Examiner) (Date)			`,				
The term of this patent shall								
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